What is claimed is:

1   1.   A multi-node system comprising:

2   a first node including a first processor and a first node controller, where said first

3   processor is to generate a request and said first node controller is to assert a signal to said first

4   processor to indicate that processing of said request is incomplete.

·1   2.   The multi-node system of claim 1 further comprising:

2   a second node controller coupled to said first node controller to receive said request.

1   3.   The multi-node system of claim 2 wherein said second node controller is part of a second

2   node including a second processor coupled to said second node controller, wherein said second

3   processor is to complete said request.

1   4.   The multi-node system of claim 2 further comprising:

2   a switching agent coupled between said first and second node controllers.

1   5.   The multi-node system of claim 4, wherein said second processor is to complete said

2   request.

1   6.   The multi-node system of claim 3, where said first node controller is to deassert said

2   signal when said request is completed at said second node.

1   7.   The multi-node system of claim 5, where said first node controller is to deassert said

2   signal when said request is completed at said second node.

1   8.   The multi-node system of claim 1 wherein said request is a purge TLB entry request.

1   9.   The multi-node system of claim 6 wherein said request is a purge TLB entry request.

· 1   10.   The multi-node system of claim 7 wherein said request is a purge TLB entry request.

1   11.   A method for processing a request in a multi-node system comprising:

2   sending a request from a first processor to a first node controller;

3   asserting a signal from said first node controller to said first processor indicating that

4   processing of said request is incomplete.

1   12.   The method of claim 11 further comprising:

2   sending said request to a second node controller in said multi-node system.

· 1   13.   The method of claim 12 further comprising:

2   completing said request for at least one processor coupled to said second node controller.

1   14.   The method of claim 13 further comprising:

2   deasserting said signal by said first node controller when said request is completed at said

3   second node.

1   15.   The method of claim 11 wherein said request is a purge TLB entry request.

1   16.   The method of claim 14 wherein said request is a purge TLB entry request.

1   17.   A method for processing a request in a multi-node system comprising:

2   sending a request from a first processor to a first node controller;

3   asserting a signal from said first node controller to said first processor indicating that

·4   processing of said request is incomplete; and

5   sending said request to a second node controller via a switching agent in said multi-node

6   system.

1   18.   The method of claim 17 further comprising:

2   completing said request for at least one processor coupled to said second node controller.

1   19.   The method of claim 18 further comprising:

2   deasserting said signal by said first node controller when said request is completed at said

3   second node.

1   20.   The method of claim 17 wherein said request is a purge TLB entry request.

1   21.   The method of claim 18 wherein said request is a purge TLB entry request.